

SAHRDAYA

COLLEGE OF ENGINEERING & TECHNOLOGY

AUTONOMOUS

KODAKARA - THRISSUR (DT) - KERALA - INDIA - 680684

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SCET/COE/M/24/12/1-1

04.12.2024

TIME TABLE

Sub: Time schedule for the First Semester Regular M. Tech. End Semester Examination December 2024 (2024 Admission) – December 2024 Notification – Reg.

Ref: Notification SCET/COE/M/24/12/1 dated 4th December 2024

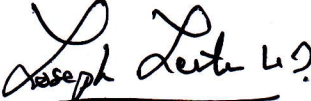
In continuation of the notification read above, the detailed timetable of the First Semester Regular M. Tech. End Semester Examination December 2024 (2024 Admission) is published herewith for the information of all concerned.

Date	Time	Exam Slot	Course Code	Course Name	M.Tech Programmes
13-01-2025 (Monday)	10:00 am - 12:30 pm	A	241TEC100	Advanced Engineering Mathematics	ECE
			241TCS100	Advanced Machine Learning	CSE
			241TBT007	Industrial Biotechnology	BT
15-01-2025 (Wednesday)	10:00 am - 12:30 pm	B	241TEC001	System Design using Embedded Processors	ECE
			241TCS001	Advanced Database Management	CSE
			241TBT008	Advanced Fermentation Technology	BT

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NBA Accredited programs (BME, BT, CSE, CE, EC) Accredited by Institution of Engineers (India);
ISO 9001:2015 Certified.

17-01-2025 (Friday)	10:00 am - 12:30 pm	C	241TEC002	Embedded Programming	ECE
			241TCS002	Foundations Of Computer Science	CSE
			241TBT009	Bioseparation Technology	BT
20-01-2025 (Monday)	10:00 am - 12:30 pm	D	241EEC002	Cloud Computing	ECE
			241ECS100	Object Oriented Software Engineering	CSE
			241EBT100	Biopharmaceutical Technology	BT
22-01-2025 (Wednesday)	10:00 am - 12:30 pm	E	241EEC009	Sensor Technologies and MEMS	ECE
			241ECS009	Natural Language Processing And Text Mining	CSE
			241EBT006	Immunotechnology And Molecular Diagnostics	BT
24-01-2025 (Friday)	10:00 am - 12:30 pm	S	241RGE100	Research Methodology And IPR	ECE,CSE,BT

Controller of Examinations
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Kodakara, Kerala-680 684, INDIA



Dr. Joseph Jestin K J
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Cc. to Principal, Administrators & HODs, All Staff Members, Examination Cell, 2024 Batch Students, Office Administration, Notice Board, College Website